

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L6	3	"6556458".pn.	US-PGPUB ; USPAT; EPO; JPO; DERWE NT	2006/01/23 16:58
2	BRS	L7	45	((ac adj2 motor) with inverter) ; and ((current near3 (detect\$6 or sens\$3 or monitor\$3)) with (supply near6 inverter))	US-PGPUB ; USPAT; EPO; JPO; DERWE NT	2006/01/23 17:08
3	BRS	L8	3	7 and ((\$5controller or \$5processor) with (angular adj (frequency or speed or position))) and (inverter near3 current)	US-PGPUB ; USPAT; EPO; JPO; DERWE NT	2006/01/23 17:04
4	BRS	L9	42	7 not 8	US-PGPUB ; USPAT; EPO; JPO; DERWE NT	2006/01/23 17:31

	Comments	Error Definition	Errors
1			
2			
3			
4			

	Type	L #	Hits	Search Text	DBs	Time Stamp
5	BRS	L10	0	(ac adj2 motor) and (pwm or modulat\$6) and ((\$5controller or \$5processor) with (((angular or positional) adj frequency) near3 inverter)) and (power with ((current near3 (sens\$3 or detect\$6 or monitor\$3)) near3 inverter))	US-PGPUB; USPAT; EPO; JPO; DERWE NT	2006/01/23 17:39
6	BRS	L11	0	318/801.ccls. and (ac adj2 motor) and (pwm or modulat\$6) and ((\$5controller or \$5processor) with ((rotat\$6 adj phase) near3 inverter)) and (power with (inverter near3 (current near3 (sens\$3 or detect\$6 or monitor\$3))))	US-PGPUB; USPAT; EPO; JPO; DERWE NT	2006/01/23 17:43
7	BRS	L12	0	318/802.ccls. and (ac adj2 motor) and (pwm or modulat\$6) and ((\$5controller or \$5processor) with ((rotat\$6 adj phase) near3 inverter)) and (power with (inverter near3 (current near3 (sens\$3 or detect\$6 or monitor\$3))))	US-PGPUB; USPAT; EPO; JPO; DERWE NT	2006/01/23 17:43

	Comments	Error Definition	Errors
5			
6			
7			

	Type	L #	Hits	Search Text	DBs	Time Stamp
8	BRS	L13	0	318/599.ccls. and (ac adj2 motor) and (pwm or modulat\$6) and ((\$5controller ; or \$5processor) with ((rotat\$6 adj phase) near3 inverter)) and (power with (inverter near3 (current near3 (sens\$3 or detect\$6 or monitor\$3))))	US- PGPUB USPAT; EPO; JPO; DERWE NT	2006/01/23 17:43

	Comments	Error Definition	Errors
8			

	Type	Hits	Search Text
1	BRS	87	inverter and controller and switch\$ and (dynamic\$ near3 brak\$) and (dissipat\$ or exhaust\$)
2	BRS	7	S1 and (step\$ near3 motor)
3	BRS	7	S1 and (step\$ near3 motor)
4	BRS	8	puzzle adj jigsaw
5	BRS	5482570	(motor nea4 (ac or asynchronous or synchronous or induction or brushless or step\$6 or reluctance))
6	BRS	297	S5 and (sensorless or (without adj2 sensor)) and inverter and (power adj supply)
7	BRS	58	S6 and inverter and ((\$5controller or \$5processor) with (pwm or modulat\$6))
8	BRS	45	S6 and ((inverter near3 current) with (detect\$6 or sampl\$4 or monitor\$3))
9	BRS	12	S7 and S8

	DBs	Time Stamp	Comments	Error Definition
1	USPAT	2006/01/23 16:53		Truncation Overflow. Return string from Server is: 5`84052`9
2	USPAT	2001/04/16 16:04		Truncation Overflow. Return string from Server is: 5`0`0`STE
3	USPAT	2001/04/16 16:05		Truncation Overflow. Return string from Server is: 5`0`0`STE
4	USPAT	2001/04/16 16:06		
5	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/05/12 14:57		
6	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/05/12 15:15		
7	US-PGPUB; USPAT; EPO; JPO; DERWENT	2006/01/23 16:53		
8	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/05/12 15:02		
9	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/05/12 15:03		

	Errors	Ref #
1	1	S1
2	1	S2
3	1	S3
4		S4
5		S5
6		S6
7		S7
8		S8
9		S9

	Type	Hits	Search Text
10	BRS	18	\$7 and (current near6 (sinusoidal or sine))
11	BRS	4	\$8 and \$10
12	BRS	476	\$5 and ((power near3 (supply or source)) with (inverter near6 (current near2 (sens\$3 or detect\$6 or monitor\$3 or sampl\$3))))
13	BRS	64	\$12 and inverter and ((\$5controller or \$5processor) with (pwm or modulat\$6))
14	BRS	21	\$13 and (current near6 (sinusoidal or sine))
15	BRS	2	\$14 and (sensorless or (without adj2 sensor)) and inverter and (power adj supply)
16	BRS	3	\$14 and (sample near2 hold)
17	BRS	3	\$14 and (current with (sample near2 hold))
18	BRS	2	\$18 and pwm
19	BRS	6	\$5 and (power near2 (supply or source)) with (inverter near6 resistor)) and ((current with (sample adj2 hold))
20	BRS	5706	\$5 and inverter and ((\$5controller or \$5processor) with (pwm or modulat\$6))
21	BRS	5706	\$20 and (pwm or modulat\$6)
22	BRS	599	\$21 and (current near6 (sinusoidal or sine))
23	BRS	37	\$22 and (current same (sample adj2 hold))

	DBs	Time Stamp	Comments	Error Definition
10	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/05/12 15:10		
11	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/05/12 15:05		
12	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/05/12 15:07		
13	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/05/12 15:09		
14	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/05/12 16:07		
15	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/05/12 15:16		
16	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/05/12 15:37		
17	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/05/12 15:38		
18	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/05/12 16:07		
19	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/05/12 16:05		
20	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/05/12 16:05		
21	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/05/12 16:07		
22	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/05/12 16:07		
23	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/05/12 16:12		

	Errors	Ref #
10		S10
11		S11
12		S12
13		S13
14		S14
15		S15
16		S16
17		S17
18		S19
19		S18
20		S20
21		S21
22		S22
23		S23

	Type	Hits	Search Text
24	BRS	8	\$23 and ((inverter near3 current) with (detect\$6 or resistor or sens\$3 or monitor\$3))

	DBs	Time Stamp	Comments	Error Definition
24	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/05/12 16:13		

	Errors	Ref #
24		S24